

PEN Experiment Trigger System (Years 2009-2010)

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π Gate Short enhances the number of pienu events in the “tail” trigger.

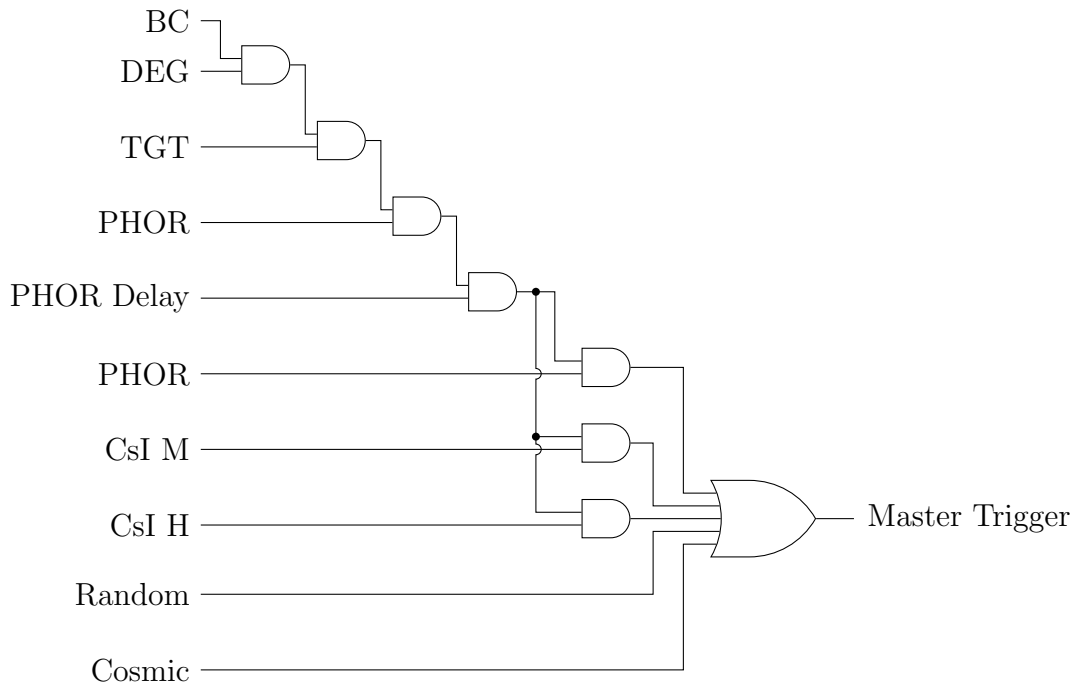


Figure 1: Trigger level 1.

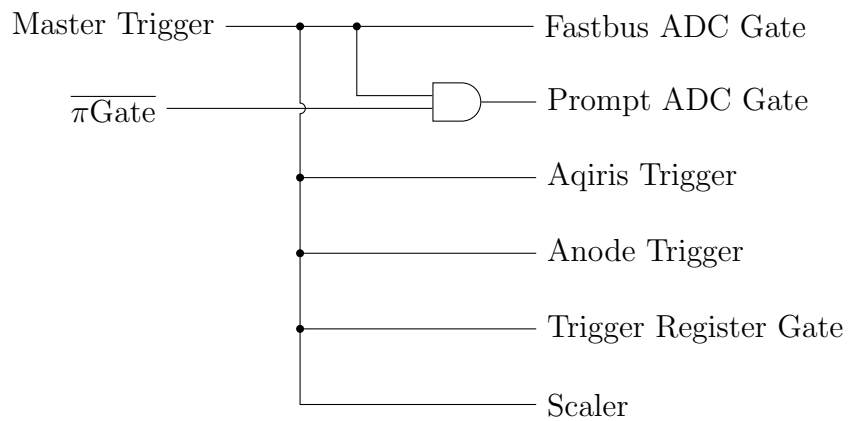


Figure 2: Trigger level 2.

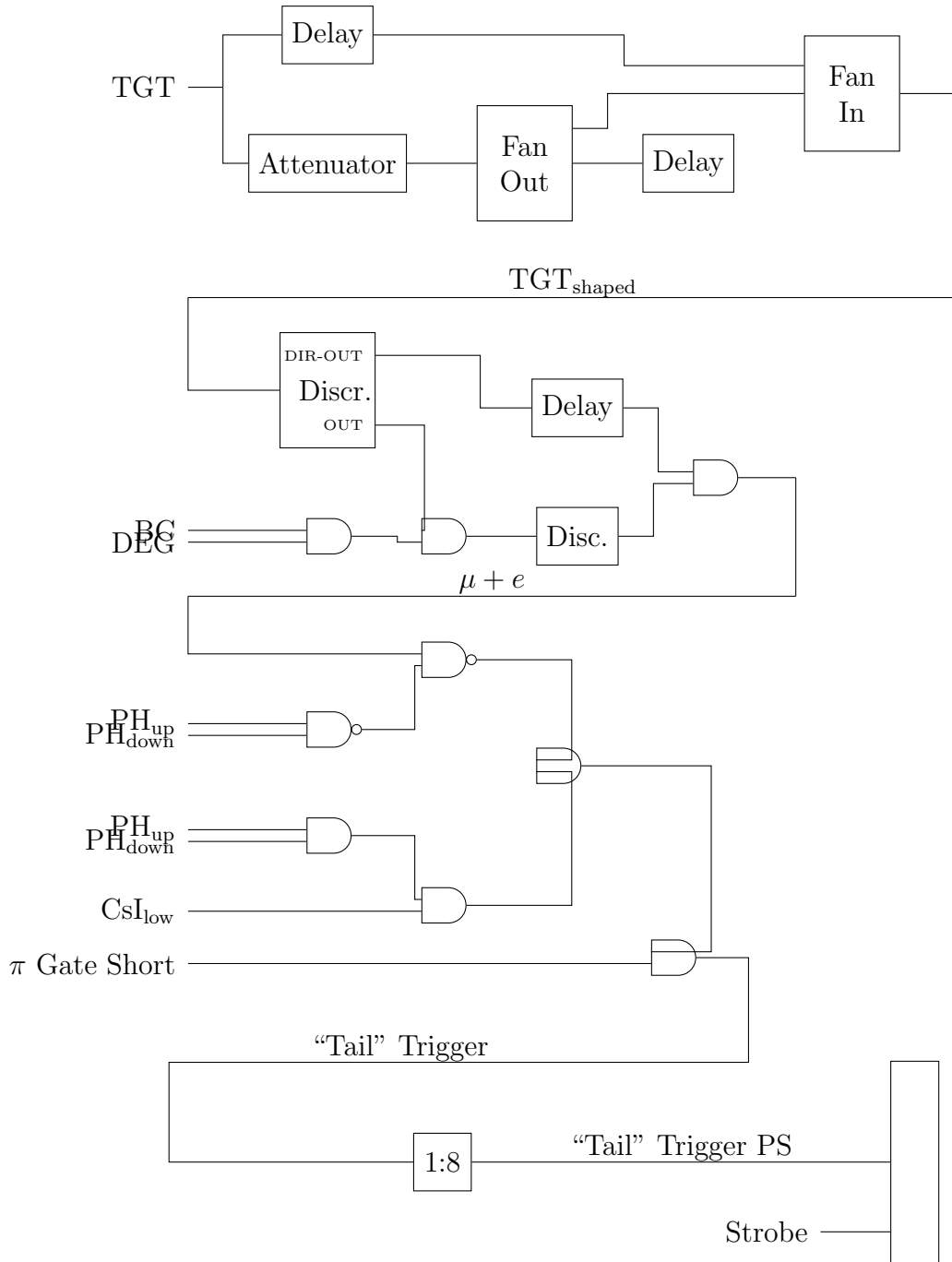


Figure 3: Trigger Level 3 (began 2009).

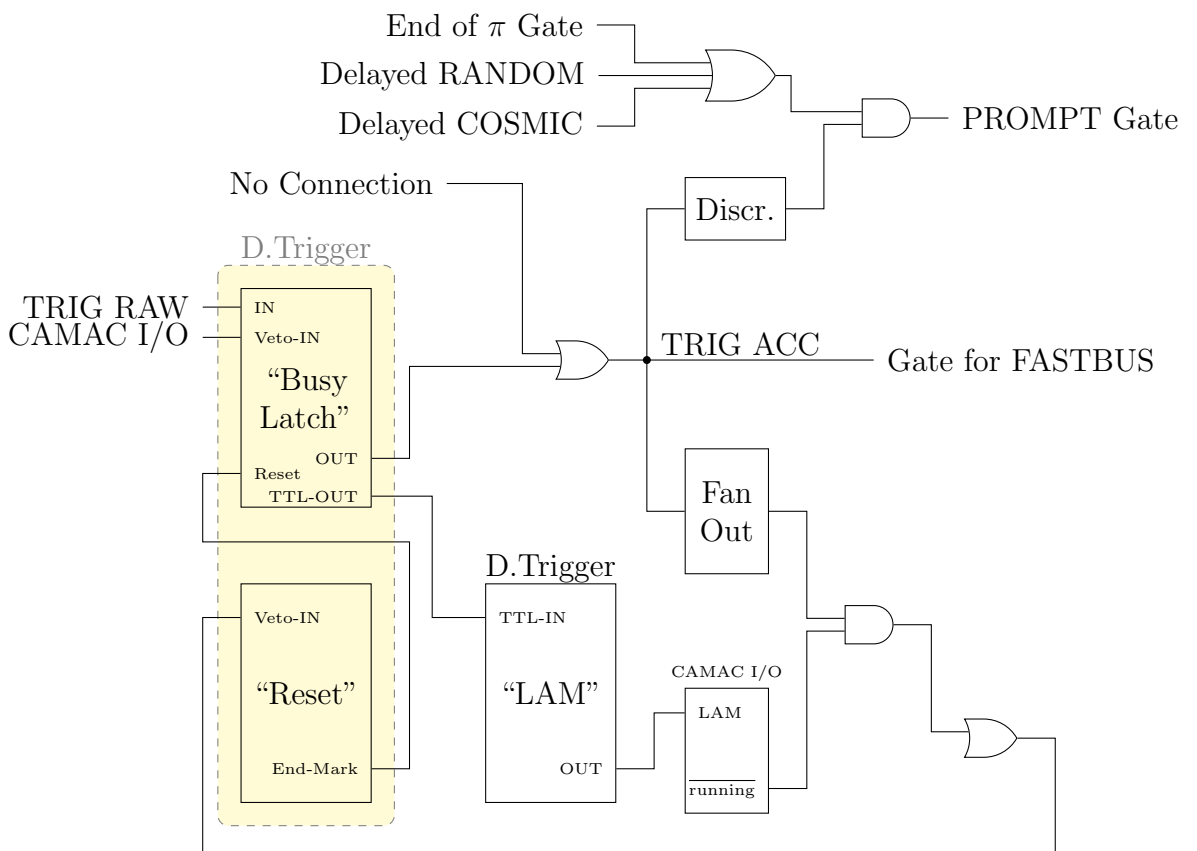


Figure 4: Busy-Latch / Reset Logic (2010).

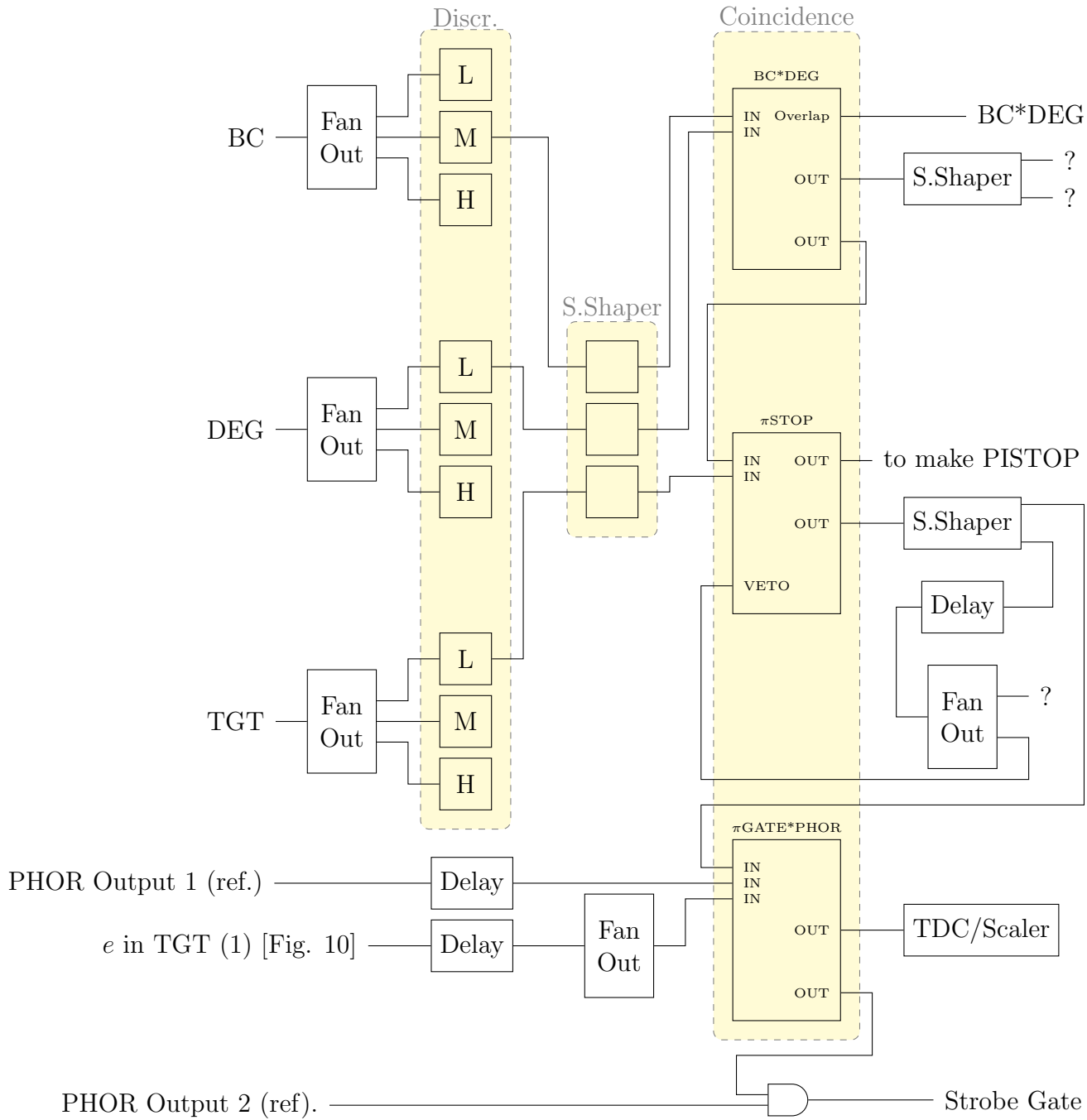


Figure 5: Logic Signal and Strobe Generation.

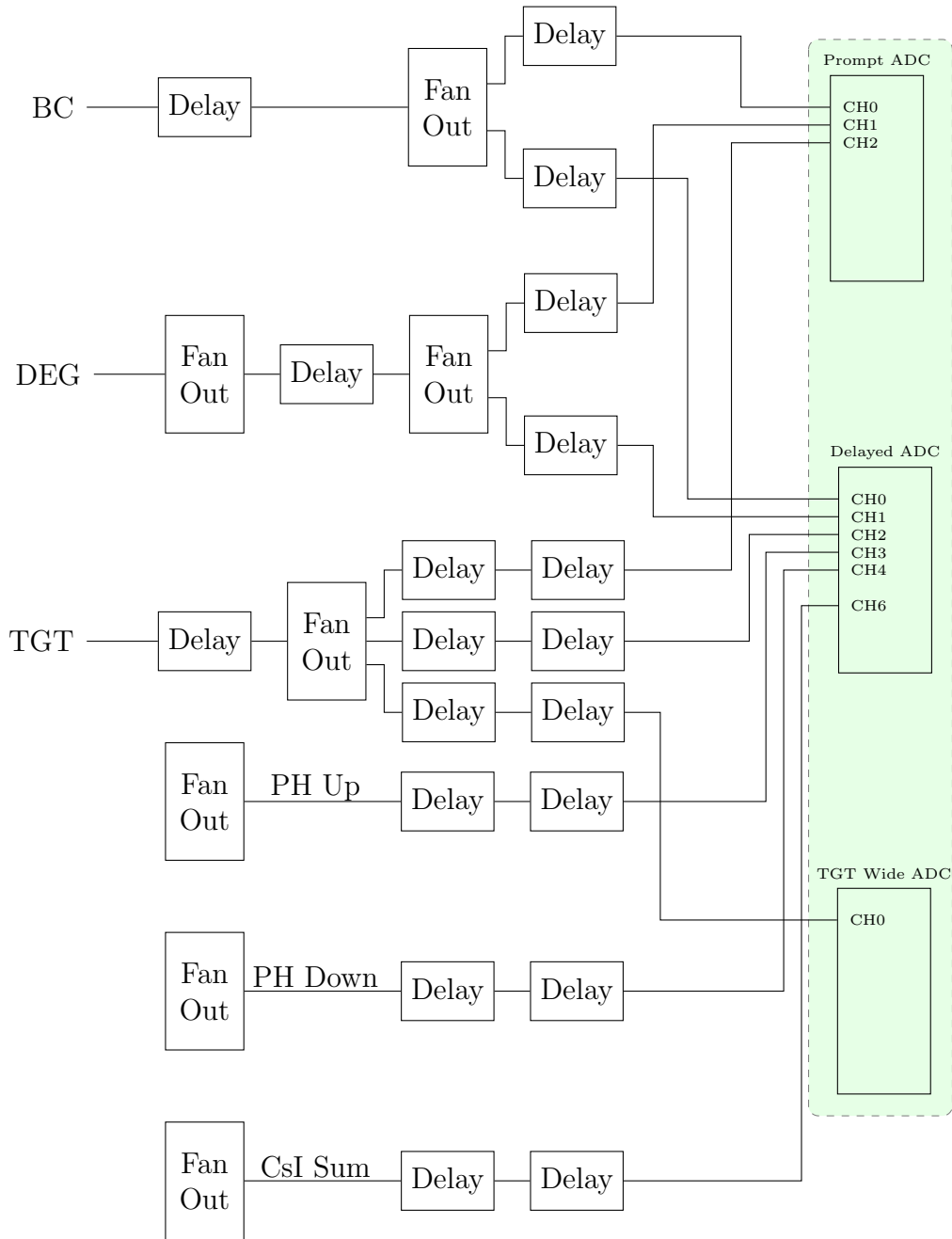


Figure 6: BADC Logic.

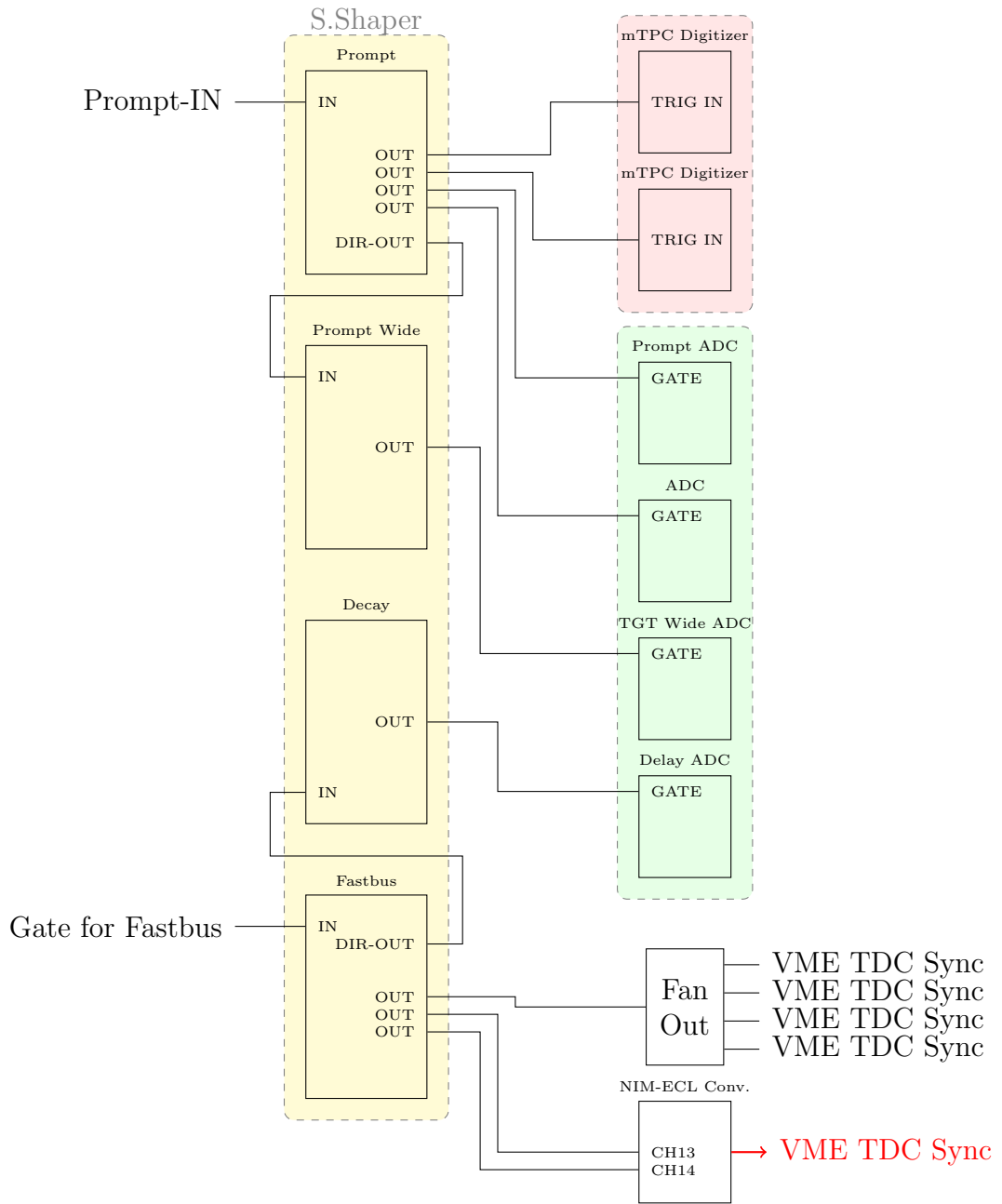


Figure 7: ADC Gate Generation.

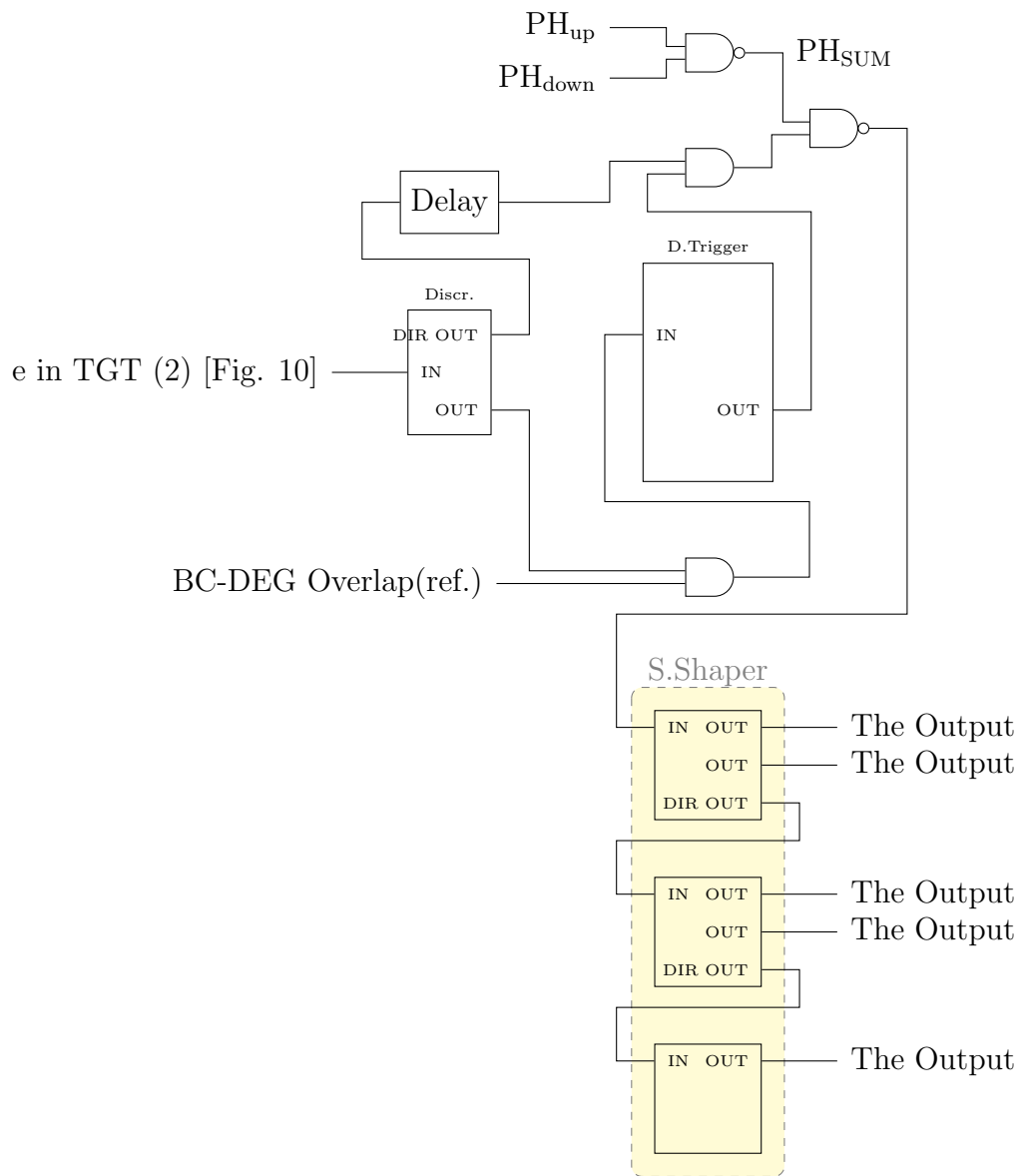


Figure 8: TRIG 0 Generation.

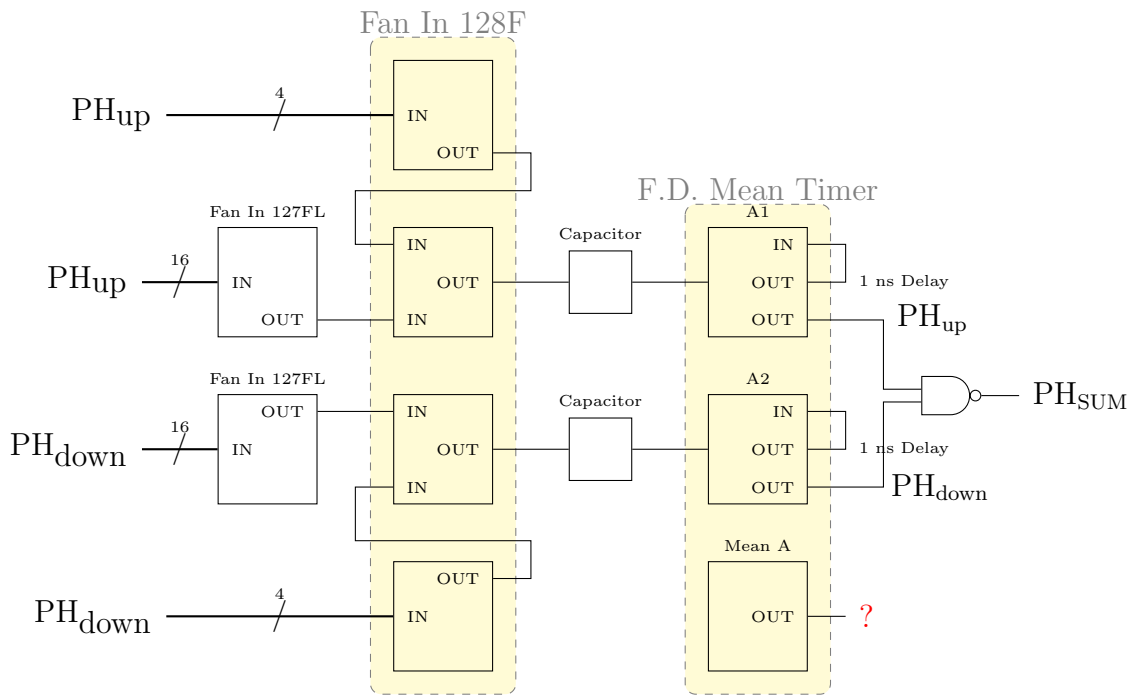


Figure 9: PH Summing.

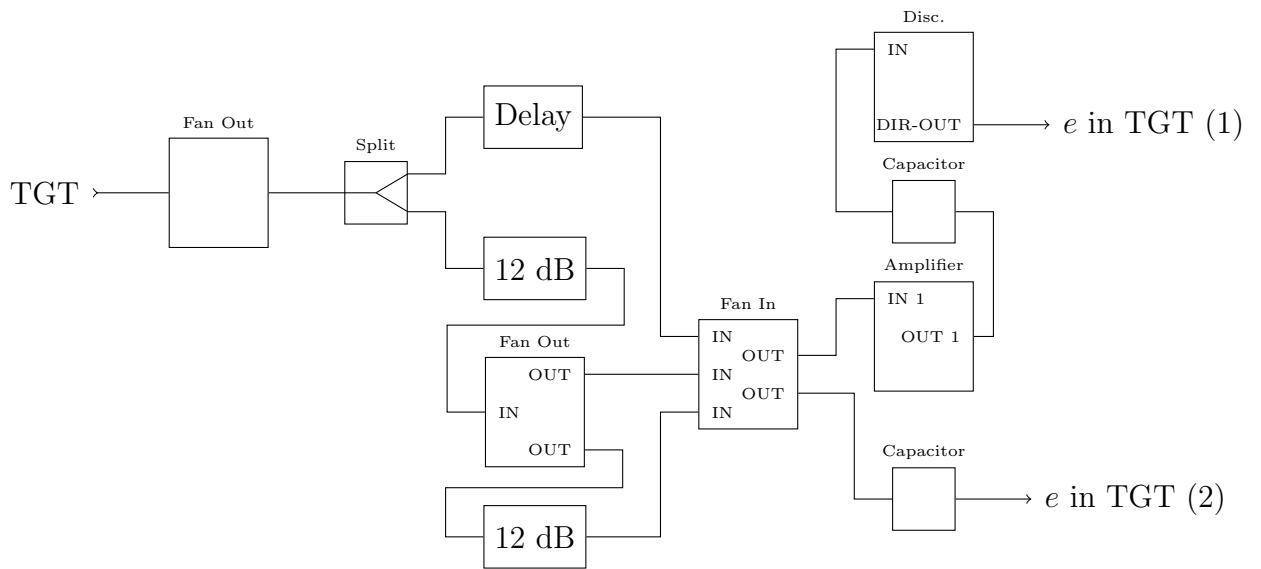


Figure 10: Target Clip Signal.

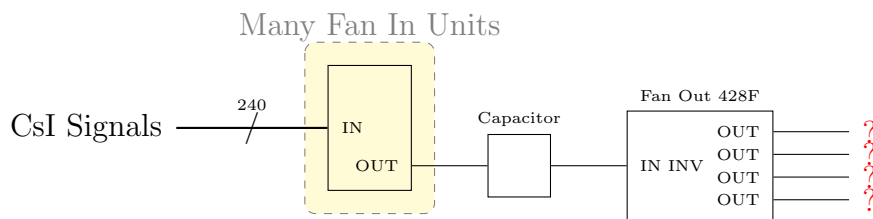


Figure 11: CsI Summing.



Figure 12: TDC / Scalers.